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## \*BIBDATASHEET\*

CONFIRMATION NO. 3511

Bib Data Sheet

SERIAL NUMBER 10/612,068	FILING DATE 07/03/2003  RULE	CLASS 438	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 67161-053
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APPLICANTS  
Koji Ida, Hyogo, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\* *have R&D*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *refused R&D*  
JAPAN 2003-022062(P) 01/30/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
\*\* 10/24/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 4	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 3
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Verified and Acknowledged  
 Examiner's Signature *[Signature]* Initials

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TITLE  
 Method of manufacturing semiconductor device

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